



IFW 2841

AMENDMENT TRANSMITTAL LETTER

Docket No.
SONYJP 3.0-217

Application No.
09/993,094-Conf. #6045

Filing Date
November 6, 2001

Examiner
H. S. Bui

Art Unit
2841

Applicant(s): Shingo Matsumoto, Yasushi Fujita, and Katsumi Endo

Invention: ADAPTER DEVICE, MEMORY DEVICE AND INTEGRATED CIRCUIT CHIP

TO THE COMMISSIONER FOR PATENTS

Transmitted herewith is an amendment in the above-identified application.

The fee has been calculated and is transmitted as shown below.

CLAIMS AS AMENDED					
	Claims Remaining After Amendment	Highest Number Previously Paid	Number Extra Claims Present	Rate	
Total Claims	13	- 20 =		x 0	0
Independent Claims	5	- 5 =		x 0	0
Multiple Dependent Claims (check if applicable)					<input type="checkbox"/>
Other fee (please specify):					
TOTAL ADDITIONAL FEE FOR THIS AMENDMENT:					0.00

Large Entity

Small Entity

No additional fee is required for this amendment.

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Andrew T. Zidel

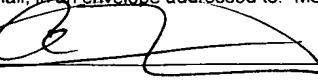
Attorney/Agent Reg. No.: 45,256

Dated: February 24, 2006

LERNER, DAVID, LITTENBERG, KRUMHOLZ & MENTLIK, LLP
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I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being deposited with the U.S. Postal Service on the date shown below with sufficient postage as First Class Mail, in an envelope addressed to: MS Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

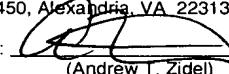
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Docket No.: SONYJP 3.0-217
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:	:
Matsumoto et al.	:
Application No.: 09/993,094	: Group Art Unit: 2841
Filed: November 6, 2001	: Examiner: H. S. Bui
For: ADAPTER DEVICE, MEMORY DEVICE AND INTEGRATED CIRCUIT CHIP	:

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT PURSUANT TO 37 C.F.R. § 1.111

Dear Sir:

In response to the Office Action mailed November 25, 2005, applicant submits the following amendments and remarks.